COMBINED NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES & PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. 1.136(a) (Large Entity)			Docket No. BUR919990190US1 (13020)		
In Re Application Of: 1	Douglas D. Coolbaugh, et al.				
Serial No.	Filing Date	Examiner	Gr	oup Art Unit	
09/516,615	March 1, 2000	Cuong Q. Ngnyen		2811	
Invention: METHOD O	OF FABRICATING A POLYSII OLYSILICON LAYERS	LICON CAPACITOR UTILIZ	ing pet		
·	TO THE COMMISS	SIONER FOR PATENTS:			
petition under the provisi September 24, 2003 Date	ce of Appeal from the Primary E ons of 37 CFR 1.136(a) to exter in the above-identified applica	nd the period for filing a respo tion.	nse to the Office	Action of.	
	al(s) to the Board of Patent App mber 24, 2003 finally rejecting		he decision of the	Primary	
Applicant(s) hereby requi	o) to emit to noisnetxe ns (e)tee	heck desired time period): Three months	onths GF	ive months	
from:1	December 24, 2003	• •	y 24, 2004 Pate		
The fee for the Notice of	Appeal and Extension of Time	has been calculated as show	n below:		
	Fee for Notice	ce of Appeal: \$330	.00		
	Fee for Exte	nsion of Time; \$420	2.00		
TOTAL FEE FOR NOTIC	E OF APPEAL AND EXTENSI	ON OF TIME: S750	.00		
The fee for the Notice of	Appeal and extension of time is	to be paid as follows:			
☐ A check in the amou	int of for the	Notice of Appeal and extension	on of time is enclo	sed.	
Please charge Depo	osit Account No. 09-0456/IBM	in the amount of \$750.00	•		
	by authorized to charge paymer redit any overpayment to Depos				
	filing fees required under 37 C.I		•		
	हैं islon of time is required, please ed to Deposit Account No.	consider this a petition therefo	or and charge an	y additional fees	
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* RCVD AT 212412004 5:34:06 PM	(Eastern Standard Time) * SVR:USPTO-EF)	(RF-140 * DNIS:8729306 * CSID:51674243 057	66° DÜRÂTION (mm-ss) /23/2024 ASNITH	01 42cenevo 02828283 098456	8
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In Re Application Of: Douglas D. Coolbaugh, et al.

Docket No. BUR919990190US1 (13020)

Serial No. 09/516,615

Filing Date March 1, 2000 Examiner Coong Q. Nguyen

Group Art Unit 2811

invention: METHOD OF FABRICATING A POLYSILICON CAPACITOR UTILIZING FET AND BIPOLAR BASE POLYSILICON LAYERS

TO THE COMMISSIONER FOR PATENTS:

This combined Notice of Appeal from the Primary Examiner to the Board of Patent Appeals and Interferenc s and petition for extension of time under 37 CFR 1.136(a) is respectfully submitted by the undersigned:

Signature

Dated: February 24, 2004

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Steven Fischman

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i certify that this document and fee is being deposited on with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PAGE 3/5 * RCVD AT 2/24/2004 5:44:06 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/0 * DNIS:8729306 * CSID:5167424366 * DURATION (mm-ss):01-42